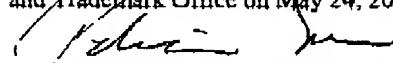


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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BOARD OF PATENT APPEALS AND INTERFERENCES**

Appellant(s): Kazunori KISHIMOTO
Serial No.: 10/083,447
Filed: February 26, 2002
For: METHOD OF TESTING A SEMICONDUCTOR
INTEGRATED CIRCUIT AND METHOD AND
APPARATUS FOR GENERATING TEST PATTERNS
Examiner: John P. Trimmings
Group Art Unit: 2138
Attorney Docket No.: NEKO 19.481 (100806-00091)

May 24, 2006

REQUEST FOR ORAL HEARING

Board of Patent Appeals and Interferences
Assistant Commissioner for Patents
Washington, D.C., 20231

Sir:

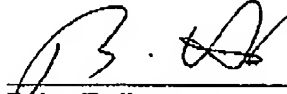
Appellants hereby request an Oral Hearing in the present appeal as Appellants consider such a hearing necessary and/or desirable for a proper presentation of the appeal.

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The Examiner's Answer was mailed on March 27, 2006, and therefore it is respectfully submitted that this Request is timely filed pursuant to 37 C.F.R. 41.47. All requisite fees may be charged to Deposit Account No. 50-1290.

Respectfully submitted,



Brian E. Hennessey
Reg. No. 51,271

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Docket No.: 100806-00091 (NEKO 19.481)
BEH:pm

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